



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:
STICKLER, ET AL.

Serial No.: 09/736,615

Group Art Unit: 2841

Filed: December 14, 2000

Examiner: J. Alcala

For: PRINTED CIRCUIT
BOARD LAYOUT

Atty Dkt: HP-10007356-1

COPY OF PAPERS
ORIGINALLY FILEDDECLARATION OF LISA ANN CASELLI

I, Lisa Ann Caselli, hereby declare:

1. I hold the degree of Bachelor of Science in Electrical Engineering from Purdue University.
2. I have been an employee of Hewlett-Packard Company for over eight years.
3. My current position at Hewlett-Packard Company, which I have held since 1996, is Hardware Design Engineer.
4. I am one of the named inventors on the above described patent application.
5. I performed a number of tests on single-ended SCSI buses on a printed circuit system board (PCSB) of a Sun Microsystems Inc. Ultra 60 Workstation.
6. The tests were conducted to determine the location of certain signal traces.
7. After conducting the tests I photographed the PCSB and provided annotations on printouts of those photographs which identify the location of certain signal traces.
8. Those photographs with my annotations accurately describe the results of my tests and are attached hereto as Appendix I.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application, any patent issued thereon, or any patent to which this declaration is directed.

Lisa Ann Caselli
Lisa Ann Caselli

5/30/02
Date

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JUN 18 2002

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